

IN THE CLAIMS

Amend the claims as follows:

*Sub
D17*
1.(Twice Amended) A rectifier, comprising:

a reference primary circuit;

a transformer having a primary side connected to said reference primary circuit and having a secondary side;

first and second rectifiers in synchronous connection at said secondary side, said first and second rectifiers each having at least three leads, one of said three leads being a control lead;

first and second clamping transistors, said first clamping transistor being connected between said control lead of said first rectifier and said secondary side, said second clamping transistor being connected between said control lead of said second rectifier and said secondary side;

a first damping resistor connected to a control lead of said first clamping transistor and a second damping resistor connected to a control lead of said second clamping transistor;

a load connected to a same winding of said secondary side as said first and second clamping transistors; and

a fixed voltage source connected to said first and second damping resistors to supply a fixed voltage to said control leads of said first and second transistors.

*Sub
D1
DJ*

3. (Amended) A rectifier as claimed in claim 1, wherein said first and second rectifiers and said first and second clamping transistors are field effect transistors and said control leads are gate leads.

*Sub
D1
C2*

11. (Twice Amended) A self-driven synchronous rectifier, comprising:

a transformer having a primary and secondary winding;

an input for an input voltage connected to said primary winding;

a pair of rectifiers connected in a synchronous connection,

 a first rectifier of said pair of rectifiers including a source drain connection in series with a first lead of said secondary winding and a gate connected to a second lead of said secondary winding;

 a second rectifier of said pair of rectifiers having a source and drain leads connected across said first and second leads of said secondary winding and a gate connected to said first lead of said secondary winding;

 a first transistor connected between said gate of said first rectifier and said second lead of said secondary winding;

 a first damping resistor connected to a control lead of said first transistor;

 a second transistor connected between said gate of said second rectifier and said first lead of said secondary winding;

 a second damping resistor connected to a control lead of said second transistor;

 a load connected to said secondary winding of said transformer; and

Dm

a voltage source connected to said first and second damping resistors to supply a voltage through said damping resistors to control leads of said first and second transistors.

Sub D17

CX

15..(Amended) A synchronous rectifier as claimed in Claim 11, wherein said first and second transistors are field effect transistors and said control leads are gate leads.